

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	§	
Duc V. Ho et al.	§	Group Art Unit: Unassigned
Serial No.: Unassigned	§	
	§	Examiner: Unassigned
Filed: Herewith	§	
	§	
For: APPARATUS AND METHOD FOR	§	Atty. Docket: MICS:0119
MANAGING VOLTAGE BUSES	§	03-0188

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

Sir:

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §§ 1.97(b)(1) AND 1.98**

In compliance with the duty of disclosure under 37 C.F.R. § 1.56(a), Applicants respectfully request that this Information Disclosure Statement be entered and that the references listed on the PTO-1449 Form be considered by the Examiner and made of record. **In accordance with the revised procedures promulgated by the Office of Patent Legal Administration and relating to applications filed after June 30, 2003, copies of the listed references are not required to be submitted to the Examiner.**

In accordance with 37 C.F.R. § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made, as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b), or

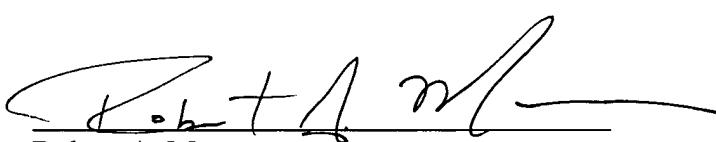
as a representation that no other possibly material information, as defined in 37 C.F.R. § 1.56(b), exists.

Furthermore, the references listed on the attached PTO-1449 Form are not to be construed as an admission that these references qualify as prior art as to the above-referenced application or any related application. Rather, these references are being presented for the Examiner's consideration without prejudice to Applicants' right to demonstrate that any of these references do not qualify as prior art should the Examiner choose to apply any of these references.

The following information is listed below in accordance with 37 C.F.R. §1.98. Any explanation of non-English language documents contained in this Information Disclosure Statement is believed to constitute a concise explanation of the relevance of the listed reference as it is presently understood by the individual designated in § 1.56(c) most knowledgeable about the content of the listed reference, in accordance with 37 C.F.R. § 1.98(a)(3).

Respectfully submitted,

Date: December 31, 2003



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Form PTO-1449 (modified)	ATTY. DOCKET NO. MICS:0119 (03-0188)	SERIAL NO. Unassigned
List of Patents and Publications For Applicant's Information Disclosure Statement	APPLICANT Duc V. Ho et al.	
(Use several sheets if necessary)	FILING DATE Herewith	GROUP Unassigned

U.S. PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A	6,586,979	07-01-2003	Gomm et al.			
	B	6,557,090	04-29-2003	Ho			
	C	6,240,028	05/29/2001	Ho			
	D						
	E						
	F						
	G						
	H						

FOREIGN PATENT DOCUMENTS

EXAM. INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION
	I						
	J						
	K						

OTHER ART

(Author, Title, Journal, Volume, Pertinent Pages, & Date)

	L	Gomm et al., Power Reduction For Delay Locked Loop Circuits, Published December 19, 2002
	M	Gomm et al., Method for Noise and Power Reduction For Digital Delay Lines; Published September 26, 2002

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.